

Substitute for form 1449A/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

**Complete if Known**

Application Number	09/966,224
Filing Date	September 28, 2001
First Named Inventor	Michael D. Ruehle
Art Unit	2136
Examiner Name	Carl G. Colin
Attorney Docket Number	42390P11974

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]Examiner  
Signature

Paul Corwin

Date	
Considered	

3/21/06

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003.

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
CU		JYH-HUEI GUO ET AL., "A Novel Digit-Serial Systolic Array for Modular Multiplication", <i>Proceedings of the IEEE International Symposium on Circuits and Systems</i> , May 31-June 3, 1998, Monterey, California, pg. II-177-180.		
CU		JYH-HUEI GUO ET AL., "Design and Implementation of an RSA Public-Key Cryptosystem", <i>Proceedings of the IEEE International Symposium on Circuits and Systems</i> , 1999, pg. I-504-507.		
CC		C.D. WALTER, "Systolic Modular Multiplication", <i>IEEE Transactions on Computers</i> , Vol. 42, No. 3, March 1, 1993, pg. 376-378, IEEE Inc., New York, US.		
CC		WEIXIN GAI ET AL., "A Systolic Linear Array for Modular Multiplication", <i>Proceedings of the IEEE 2nd International Conference on ASIC</i> , October 1996, pg. 171-174.		

Examiner Signature		Date Considered	3/21/06
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